



1746
Patent
261/136
P96-0015US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Eric J. Bergman

Serial No.: 09/811,925

Filed: March 19, 2001

For: Methods for Cleaning Semiconductor Surfaces

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) **Group Art Unit: 1746**
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) **Examiner: Z. El-Arini**
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B17
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SUPPLEMENTAL STATEMENT OF RELATED APPLICATIONS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Sir:

This Application is a Continuation of Serial No. 08/853,649, now U.S. Patent No. 6,240,933

(enclosed). The following are Continuation-in-Part Applications of 08/853,649:

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CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

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Patent
261/136
P96-0015US2

<u>Serial No.</u>	<u>Filing Date</u>	<u>Examiner</u>	<u>Art Unit</u>
09/536,251 J	03/27/00	F. Stinson	1746
09/677,934	10/02/00	F. Stinson	1746
Pat. 6,267,125 J			
09/677,929	10/02/00	F. Stinson	1746
Pat. 6,273,108 J			
09/677,925 J	10/02/00	F. Stinson	1746
09/837,722 J	04/18/01	F. Stinson	1746
09/621,028 J	07/21/00	Carbrillo Sharidan	1746

The claims from these Applications are enclosed.

Respectfully submitted,
LYON & LYON LLP

Dated: October 22, 2001

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